

EAST Search History

Interference Search History Printout (UPDATED)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L22	0	((FPGA or field \$1programmable\$1gate \$1array or (programmable near3 logic) or (gate near3 array)) and ((control near2 signal with ("at the same time" concurr \$5 simultaneous\$3 coincident coinciding) with ((programmable configurable) near2 (block element cell module))) and (routing interconnect tracks))). clm.	US-PGPUB	OR	ON	2008/05/19 12:12
	claim 6					

5/ 19/ 2008 12:27:15 PM

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wsp